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**1 A dual bias-feed circuit design for SiGe HBT low-noise linear amplifiers**

Taniguchi, E.; Ikushima, T.; Itoh, K.; Suematsu, N.;

Microwave Theory and Techniques, IEEE Transactions on , Volume: 51 , Issue: 2 , Feb. 2003

Pages:414 - 421

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**2 A microwave miniaturized linearizer using a parallel diode with a bias feed resistance**

Yamauchi, K.; Mori, K.; Nakayama, M.; Mitsui, Y.; Takagi, T.;

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**3 Feedback stability of charge amplifiers with continuous reset through forward-biased diode junctions**

Fasoli, L.; Fiorini, C.; Bertuccio, G.;

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**5 A single-bias diode-regulated 60 GHz monolithic LNA**

*Maruhashi, K.; Ohata, K.; Madihian, M.;*

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*Chan-Wang Park; Beauregard, F.; Carangelo, G.; Ghannouchi, F.M.;*

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**10 A 60-GHz-band monolithic HJFET LNA incorporating a diode-regulated self-bias circuit**

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